

7.1.6.3 Test point signals

The digital A1 PCB is provided with test points, marked: "TP" See figure 10.1: A1 PCB layout (component side). These can be used to check correct functioning of the PCB.

All measurements are made in the default MASTER RESET condition (start the measurements in the ScopeMeter using **MASTER RESET**).


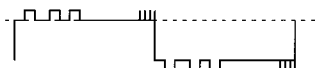

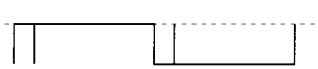
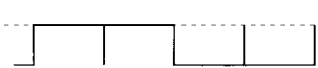



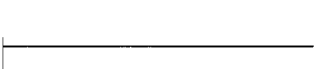



A MASTER RESET is performed as follows:

1. Remove all signals from the ScopeMeter.
2. Turn off the ScopeMeter.
3. Hold down the LCD key and press the ON/OFF key simultaneously. Two beeps are audible, and all volatile memories (RAM with battery backup) are reset. The ScopeMeter is automatically set to the METER mode.

Use another oscilloscope with high input impedance and 10:1 probe to measure the signals on the test points. See table 7.1:

Table 7.1. Overview on Test Points on the digital A1 PCB

Logic 0=0V, Logic 1=+5V

TP	Name	Scope	Freq. Per.	Data H/L/A	Description
207	Y40		595 Hz	A	Output 40, D1404
208	Y120		595 Hz	A	Output 120, D1406
209	Y200		595 Hz	A	Output 200, D1407
210	X40		595 Hz	A	Output 40, D1401
211	X120		595 Hz	A	Output 120, D1402
212	X200		595 Hz	A	Output 200, D1403
213	WEN		≈0.2 μs	A	Write Enable Not, point 10 of D1203
214	REN		≈2 μs	A	Read Enable Not, point 11 of D1203
216	TEST1		0	H	TEST 1/analog channel 3, point 16 of D1201
217	TEST2		12.1 kHz	A	TEST 2/timer 2 clock, point 64 of D1201
219	ON_OFF		0	L	ON OFF/high speed input 0.2, point 53 of D1201
221	POWER_ON		0	H	POWER ON